

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	09633087
Filing Date	2000-08-04
First Named Inventor	Derrick R. Meyer
Art Unit	2157 2457
Examiner Name	Burgess, Barbara N.
Attorney Docket Number	5500-98300

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	2	4725834		1988-02-16	Change, et al.	
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	9	5495481		1996-02-27	Duckwall	
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	2	Gustavson, David B., "The Scalable Coherent Interface and Related Standards Projects," IEEE Micro, 1992, p. 10.					<input type="checkbox"/>
	3	Cox, et al., "Adaptive Cache Coherency for Detecting Migratory Shared Data," Proc. 20th Annual Symposium on Computer Architecture, May 1993, pp. 98-108.					<input type="checkbox"/>
	4	Stenstrom, et al., "An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing," Proc. 20th Annual Symposium on Computer Architecture, May 1993 IEEE, pp. 109-118.					<input type="checkbox"/>

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	5	Wolf-Dietrich Weber, et al. "Analysis of Cache Invalidation Patterns in Multiprocessors", Computer Systems Laboratory, Stanford University, CA, pp. 243-256.	<input type="checkbox"/>
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	7	Li, et al. "Memory Coherence in Shared Virtual Memory Systems," 1986, Proceedings of the fifth annual ACM symposium on Principles of distributed computing ACM, pp. 229-239.	<input type="checkbox"/>
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	9	Hagersten, et al., "Simple COMA," Ashley Saulsbury and Anders Landin Swedish Institute of Computer Science, Jul. 1993, pp. 233-259.	<input type="checkbox"/>
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	11	Anderson, "The Performance of Spin Lock Alternatives for Shared-memory Multiprocessors", IEEE Transactions on Parallel and Distributed Systems, vol. 1, No. 1, Jan. 1990.	<input type="checkbox"/>

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